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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/627,878	07/28/2003	Yoshihiko Minatani	116230	3608
25944	7590	03/01/2006		EXAMINER
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			DEMAKIS, JAMES A	
			ART UNIT	PAPER NUMBER
			2836	

DATE MAILED: 03/01/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary	Application No.	Applicant(s)
	10/627,878	MINATANI, YOSHIHIKO
	Examiner	Art Unit
	James A. Demakis	2836

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 28 July 2003.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-8 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 28 July 2003 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____.
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
Paper No(s)/Mail Date _____.	6) <input type="checkbox"/> Other: _____.

DETAILED ACTION***Double Patenting***

1. Claim 1 is provisionally rejected on the ground of nonstatutory obviousness-type double patenting as being unpatentable over claim 1 of copending Application No. 10/369111. Although the conflicting claims are not identical, they are not patentably distinct from each other because each discloses switching power supplies with feedback from the secondary to control the output voltage. In the present Application, the claims do not a motor controller; therefore it would have been obvious to one of ordinary skill in the art at the time of the invention to have provided protection to a broad range of devices instead of only a motor drive control apparatus.

This is a provisional obviousness-type double patenting rejection because the conflicting claims have not in fact been patented.

Claim Objections

2. Claims 3-4 are objected to because of the following informalities:

The term "duty" in claims 3-4 is used by the claims to mean "duty-cycle" or "duty factor", which is the accepted meaning.

Please correct using accepted terminology.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-8 are rejected under 35 U.S.C. 102(b) as being anticipated by Takahashi; JP7095724.

Takahashi discloses a power supply circuit unit 10-1 which maintains the output with high accuracy by inhibiting switching when one of the auxiliary rectifiers and one of the intermediate output monitoring means detect abnormality in the output of a rectifier in any one of a plurality of PWM switching type power supply circuit units connected in parallel and on the secondary sides of the transformers. The outputs of a plurality of power supply circuit units 10-n, in each of which a PWM control circuit 4-n controls the switching of an AC power supply 1-n and a transformer 2-n converts the voltage from the power source 1-n into a prescribed voltage; rectifier circuits 3-n output prescribed DC voltages, all connected in parallel. An output monitoring means 5-n monitors the presence/absence of abnormality in the output of the circuit 3-n. An auxiliary rectifier 6-n and intermediate output monitoring means 7-n, which monitors the presence/absence of abnormality are connected to the secondary side of the transformer 2-n. When an abnormality signal is inputted to the abnormal power supply detecting means 8-n from circuits 5-n and 7-n, the detecting means 8-n inhibits the PWM switching of the unit 10-n by outputting a detect signal.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James A. Demakis whose telephone number is 571.272.2050. The examiner can normally be reached on 7:30AM-4:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on 571.272.2800 ext. 36. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James A. Demakis

February 17, 2006



BRIAN SIRCUS
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800